

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Rule 53(b) Divisional Application of:

Takashi FUSHIE et al

Serial No.: (Div. of Serial No.09/579,270
filed May 26, 2000)

Group Art Unit: 2841(prior)

Filed: December 6, 2001

Examiner: Patel, I. (prior)

For: MULTILAYER PRINTED CIRCUIT BOARD AND THE MANUFACTURING
METHOD

INFORMATION DISCLOSURE STATEMENT
PURSUANT TO 37 CFR 1.97(b)

Commissioner for Patents
Washington, D.C. 20231

December 6, 2001

Sir:

This Information Disclosure Statement is being filed in order to comply with Applicant's duty of disclosure under 37 CFR 1.56. The documents listed on the Form PTO-1449 were made of record in parent application Serial No. 09/579,270.

The Commissioner is authorized to charge our Deposit Account No. 01-2340 for any fee which is deemed by the Patent and Trademark Office to be required to effect consideration of this statement.

Respectfully submitted,

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Enclosures: PTO-1449

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U.S. PATENT DOCUMENTS

Examiner Initial		Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
_____	AA	5,936,843	Ohshima et al	08-1999			
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_____	AJ	10093222 A	04/10/1998	Japan	Abstract
_____	AK	11087873	03/30/1999	Japan	Abstract
_____	AL				
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OTHER DOCUMENTS

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Examiner

Date Considered